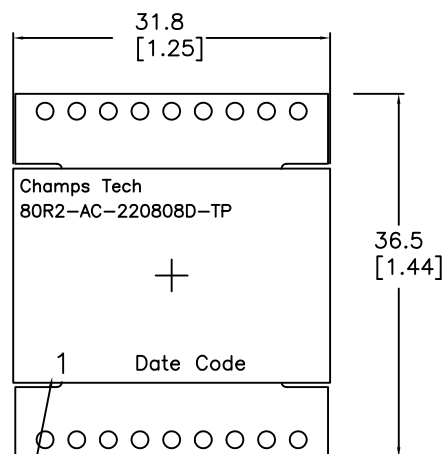
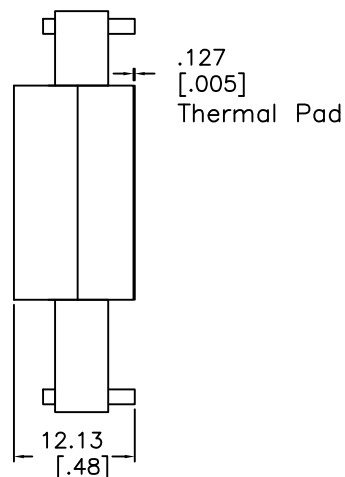


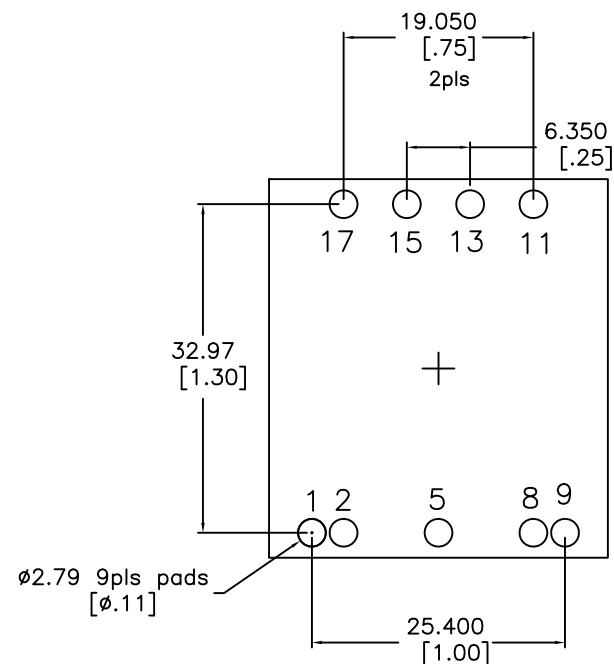
Mechanical Dimensions [Top View]



- Mark #1 on Core



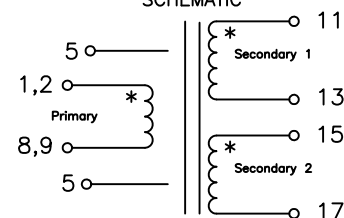
Suggested SM Layout [PCB Top View]



ELECTRICAL INFORMATION:

1. TURNS RATIO [11-13] : [1,2-8,9] = 0.363 $\pm$ 2%, [15-17] : [1,2-8,9] = 0.363 $\pm$ 2%
2. DCR [1,2-8,9] = 74 mohm Nom, 88 Max 60 Min
DCR [11-13] & [15-17] = 25 Mohms Nom, 35 Max
3. Inductance [1,2-8,9] = 2.6 mH Nom, $\pm$ 25% at 100KHz, 1.0 VRMS @ 25C
4. Leakage Inductance [1,2-8,9] = Short 11 to 17 = 2.5 uH Nom, 3.5 Max @100 KHz
5. Capacitance [1,2,8,9] : [5] = 280pF Nom, 365 Max || [11,15] : [5] = 170pF Nom, 225 Max || [1,2,8,9] : [11,15] = 105pF Nom, 135 Max
6. Dielectric Strength 3 Sec Min 1,2-8,9 : 5 : Core > 1000 VDC || 5 : Core > 1000 Vdc
Dielectric Strength 1,2,8,9,5: 11,13,15,17 > 1500 VAC || 11,13 : 15,17 > 500 Vdc
7. RoHS & REACH Compliant I Pin Composition 96/4 Tin/Silver Plating
8. Temperature Rating: -55C to +130C [Inclusive of Temp Rise]

SCHEMATIC



				CHAMPS TECHNOLOGIES	THIRD ANGLE PROJECTION 	TOLERANCES UNLESS OTHERWISE INDICATED .XXX ± .25 .XX ± 0.38 .X ± 0.78	DRAWN	HE	02.15.17	PN: 80R2-AC-220808D-TP		ISSUE A	REV 00		
							CHKD								
							APPR			SIZE		SCALE 150%			
No.	REVISIONS	DATE	APPR												