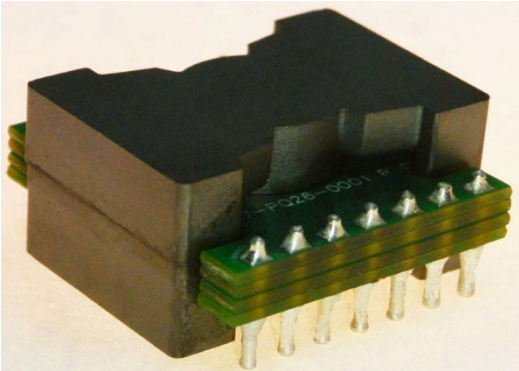


DC2306A Reference Design [Linear PoE 54Vout Ref Designs]

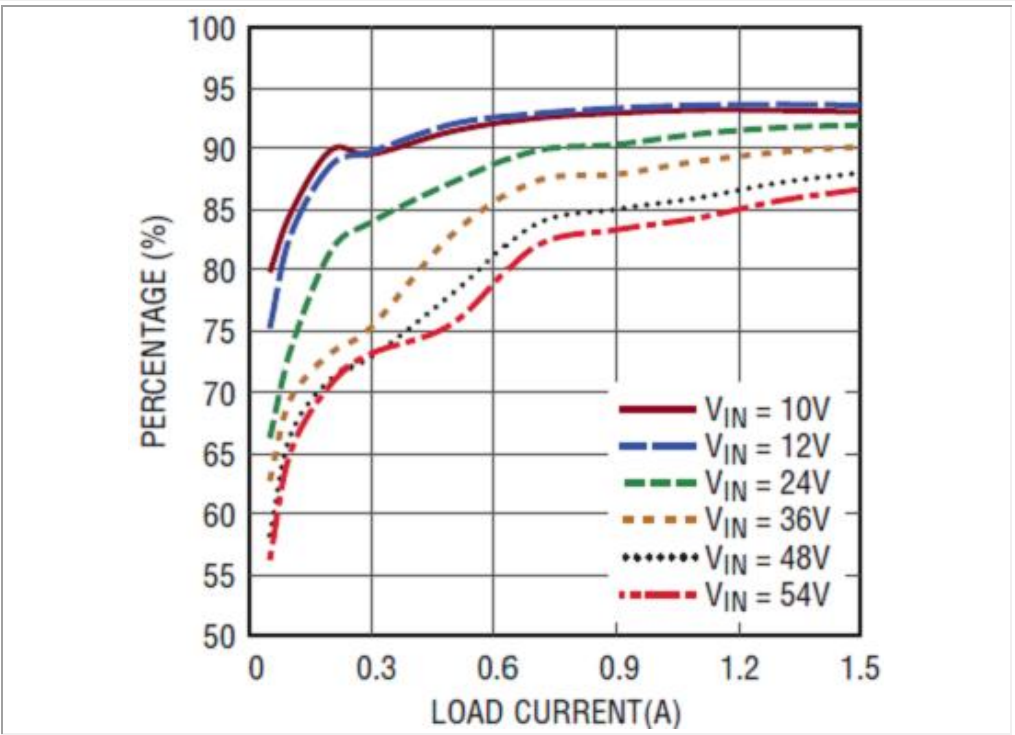
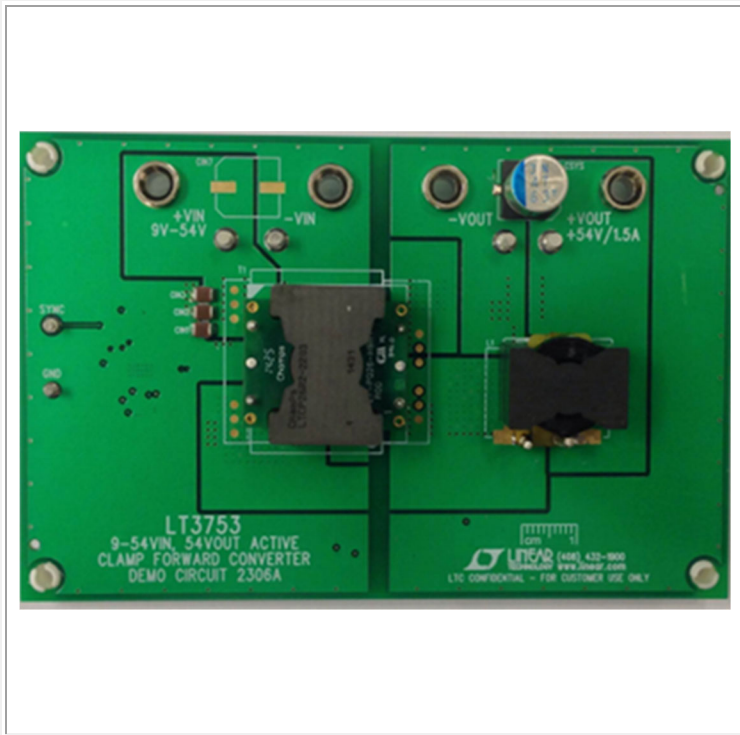
9-15Vin || 20-60Vin || 36-72Vin || 50-150Vin || 85-300Vin



- Forward Active Clamp Topology -- Highest Efficiency. Planar Design.
- Aggressive Interleave planar construction -- lowest achievable Leakage Inductance.
- Multilayer PCB optimization for lowest AC resistance and Proximity Effect.
- Wide variety of Turns Ratios in stock.
- Contact Us for DC-DC Module Design
- Contact Us for SM Assembly of all Components for DC-DC Converter

1. Input Voltage Range 10-54Vin. Output Voltage 48V or 54V at 1.5A.

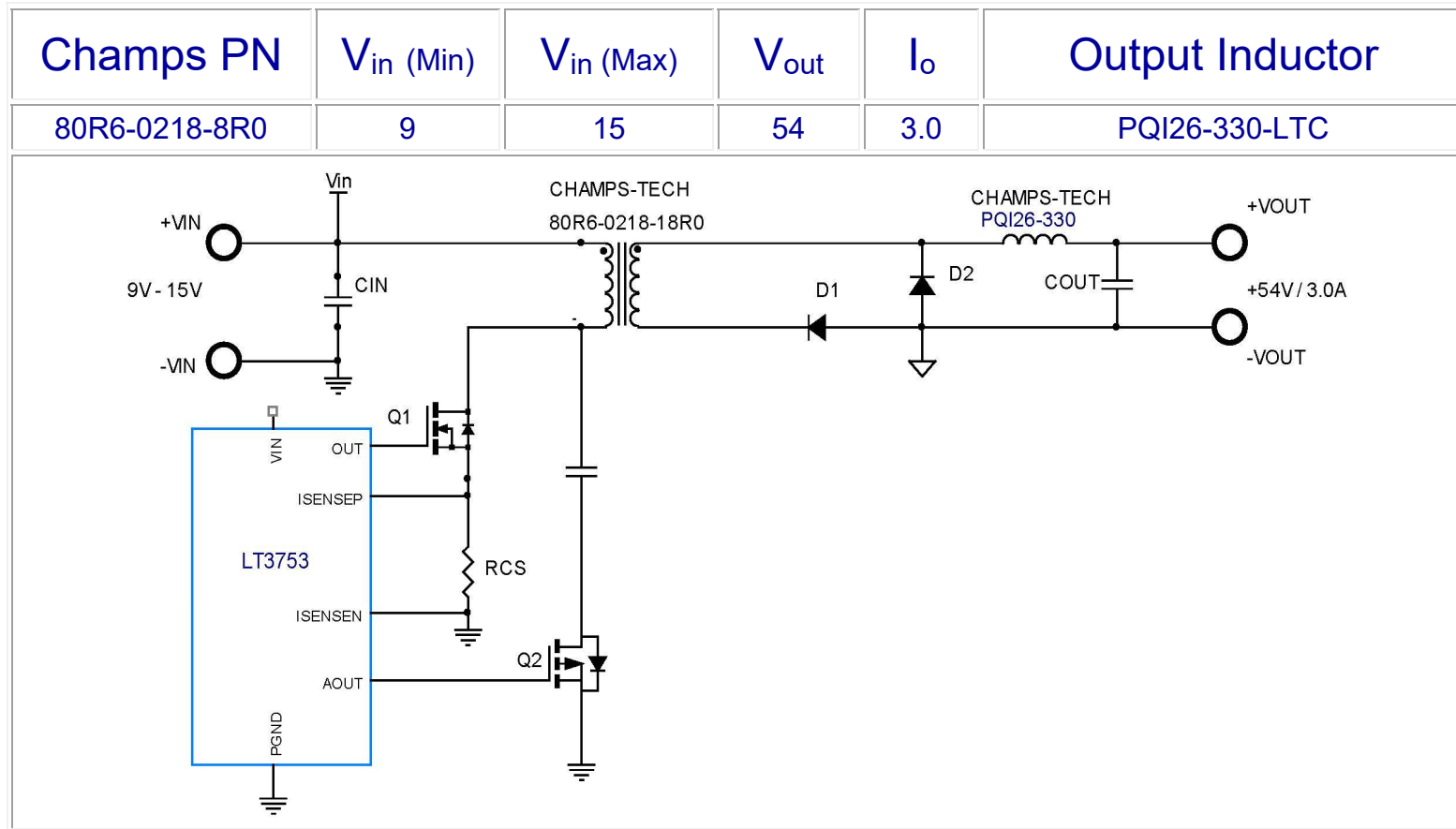
Champs PN	V_{in} (Min)	V_{in} (Max)	V_{out}	I_o	Output Inductor
P26R2-0322-18R0	10	54	54	1.5	PQA2050-330-LTC

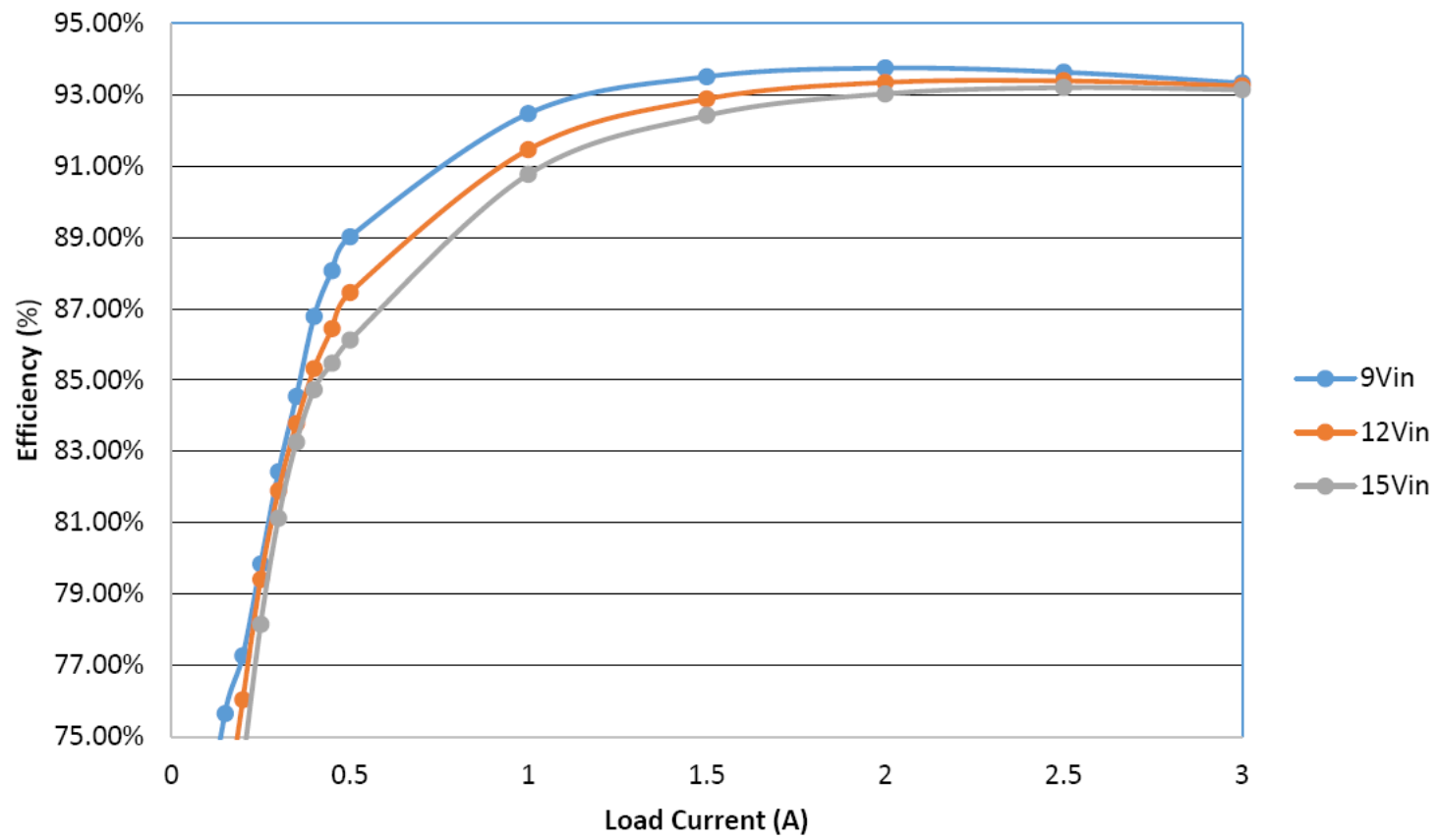


Linear Technology URL: <http://www.linear.com/solutions/5885>

©Linear Technology Inc

2. Input Range 9-15Vin. Output Voltage 54V at 3A.





[Linear Technology URL: http://www.linear.com/solutions/5650](http://www.linear.com/solutions/5650)

©Linear Technology Inc

3. Ref Design Input Range 20-60. Output Voltage 54V at 3A.

Champs PN	V_{in} (Min)	V_{in} (Max)	V_{out}	I_o	Output Inductor
80R6-0416-S03	20	60	54	3.0	PQI26-330-LTC
P26R6-0416-S03	20	60	54	3.0	PQI26-330-LTC

4. Ref Design Input Range 19-29. Output Voltage 48V at 2A.

Linear Technology URL: <http://www.linear.com/solutions/5249>

Champs PN	V_{in} (Min)	V_{in} (Max)	V_{out}	I_o	Output Inductor
55R2-8804-xx-A11	19	29	48	2.0	PQA2050-220-LTC

5. Ref Design Input Range 36-72. Output Voltage 54V at 3A.

Champs PN	V_{in} (Min)	V_{in} (Max)	V_{out}	I_o	Output Inductor
80R2-0614	36	72	54	3.0	PQI26-330-LTC
P26R2-0614	36	72	54	3.0	PQI26-330-LTC

6. Ref Design Input Range 50-150. Output Voltage 54V at 3A.

Champs PN	V_{in} (Min)	V_{in} (Max)	V_{out}	I_o	Output Inductor
80R6-0814-S02	50	150	54	3.0	PQI26-330-LTC
P26R6-0814-02-S01	50	150	54	3.0	PQI26-330-LTC

7. Ref Design Input Range 60-170. Output Voltage 54V.

Champs PN	V_{in} (Min)	V_{in} (Max)	V_{out}	I_o	Output Inductor
P26R6-1016-02-S01	60	170	54	3.0	PQI26-330-LTC
D26R6-1226-03	60	170	54	1.4	PQA2050-330-LTC

8. Ref Design Input Range 85-300. Output Voltage 48V.

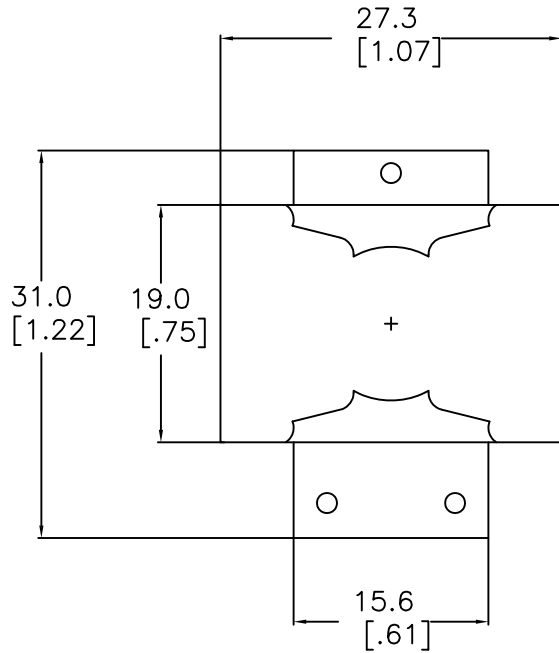
Champs PN	V_{in} (Min)	V_{in} (Max)	V_{out}	I_o	Output Inductor
P26R6-1814	85	300	48	3.5	PQI26-220-LTC

Options include discrete component or integrated complete DC-DC Converter Module:

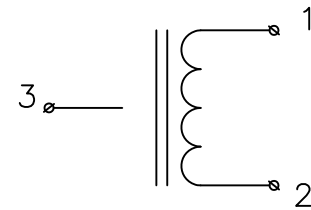
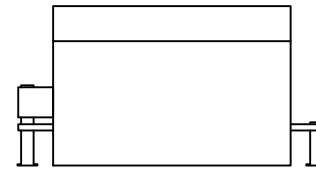
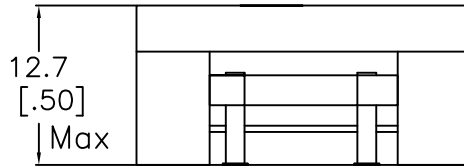
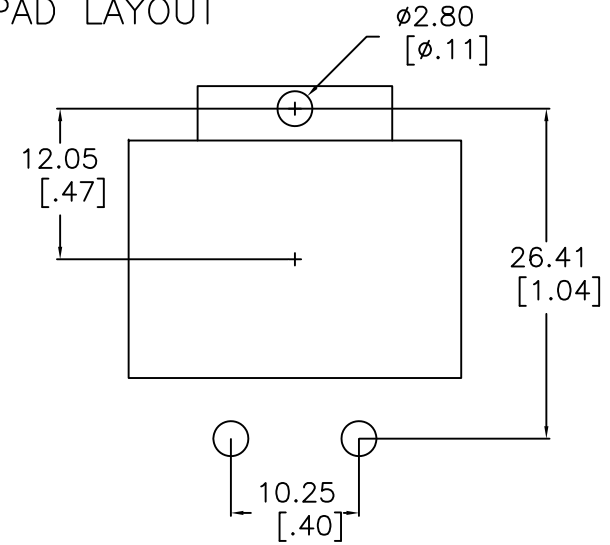
- **Surface Mount Discrete Component Design.**
- **Discrete Component Implemented in Pad-to-Pad Mounting.**
- **Component implemented as Half-Embedded Design + SM Assembly of all components required of DC-DC Converter.**
- **Implemented as a Fully Embedded Design + SM Assembly of all components required of DC-DC Converter.**
- **SMT Component Assembly of PCB Including Planar Magnetics Inclusive of Converter Testing. Volume capacity 100K per month**

Notes:

1. Consult Linear Tech Ref Design BOM and Schematic for exact device as specified for use by Linear in that Reference Design.
2. In all cases Champs Technologies makes no representation as to suitability of the Reference Design itself as that is the design responsibility and Intellectual Property of Linear Technology.
3. Champs Technologies responsibility is limited to the use of its component as described in the Data Sheet and any warranty express or implied is limited to component replacement if found defective.



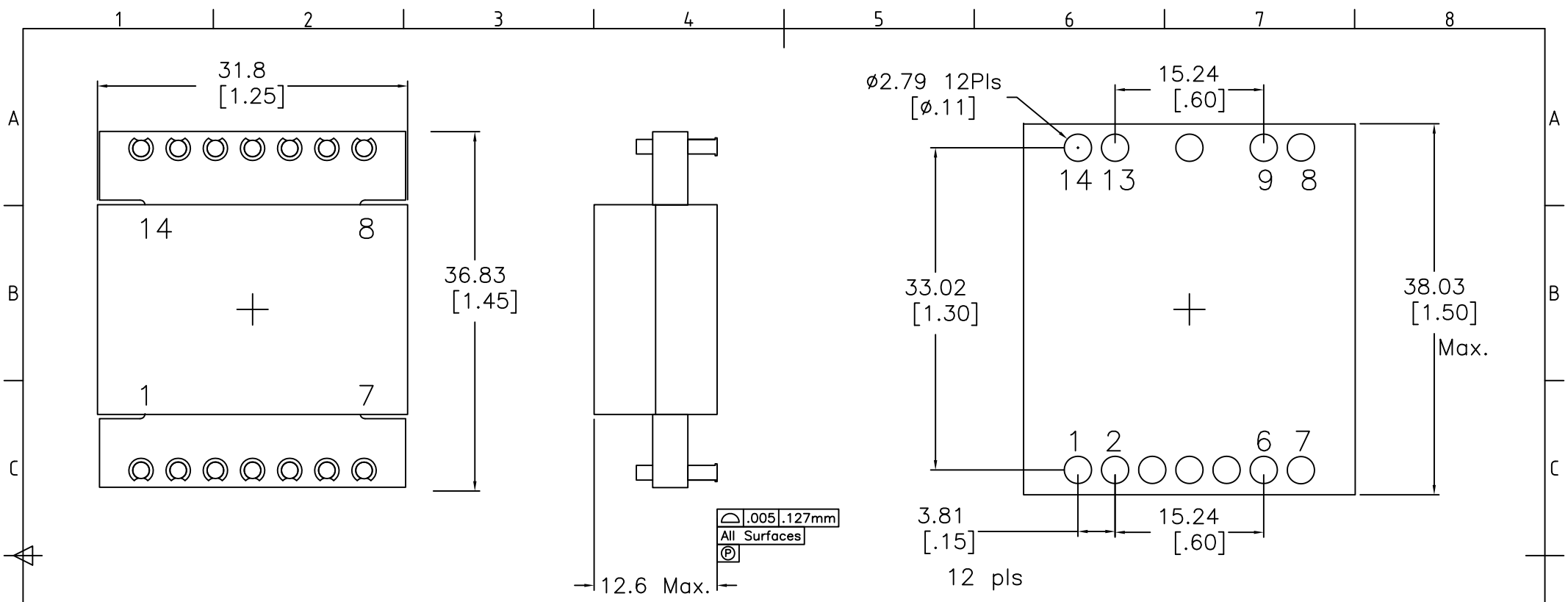
SUGGESTED PAD LAYOUT



NOTES:

1. INDUCTANCE [1-2] = 330 uH Nom, ±10% @100kHz 1.0V 4.0 Adc
2. INDUCTANCE [1-2] = 280 uH Min @100kHz 1.0V 4.8 Adc @25C
3. DCR [1-2] = 69 mohms Nom, 81 Max
4. DIELECTRIC ISOLATION > 500 VDC [1-2], : CORE
5. SATURATION CURRENT @25C = 4.8 Adc | @100C = 4.0 Adc
6. HEATING CURRENT FOR 45C RISE AT 25C AMBIENT = 6 Adc
7. Operating Ambient Temperature: -55C to +130C [Inclusive of Temps Rise]
8. RoHS Level 6/6 Compliance || 96/4 Sn/Ag Pin Composition

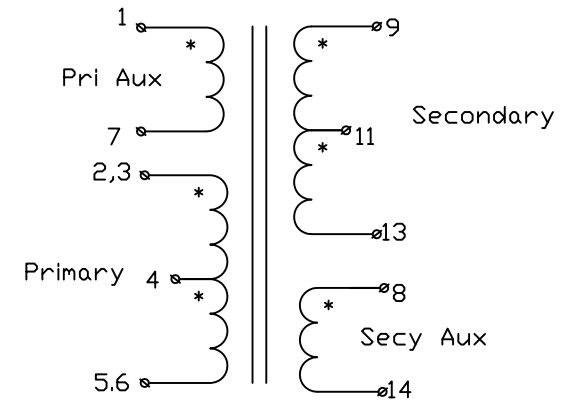
No.	DESCRIPTION	REVISIONS	DATE	APPR
CHAMPS TECHNOLOGIES				
DRAWN		SIGN	DATE	Champs No. PQI26-330-LTC
CHKD				Customer ISSUE
APPR			09.18.15	Part #: A REV
			SIZE	SCALE 2:1



Electrical Information:

1. TURNS RATIO [2-6] : [9-13] = 0.11 +/-2%
2. INDUCTANCE [2-6] = 8 uH ±10% @100kHz/1.0V
3. LEAKAGE INDUCTANCE [2-6] : SHORT 9,13 = 30nH Nom @100kHz
4. DCR [2,3-5,6] =0.7 mohms Nom, DCR [9-13] = 51 mohms Nom,
5. CAPACITANCE 2,6 to 9,13 = 170 pF Nom @100kHz
6. DIELECTRIC ISOLATION: [2,6] : [9,13] : Core > 2250 Vdc
 DIELECTRIC ISOLATION: [2,6] : [9,13] : Core > 1500 VAC
 DIELECTRIC ISOLATION: [9,13] : CORE > 500 Vdc
7. RoHS Level 6/6 Compliant | Pins 96/4 Sn/Ag Plating

Schematic

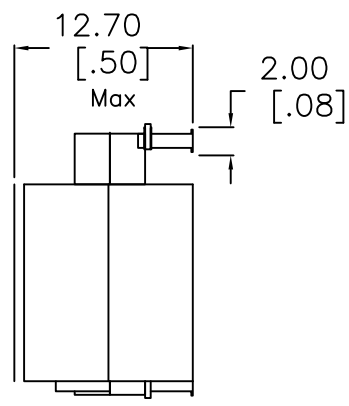
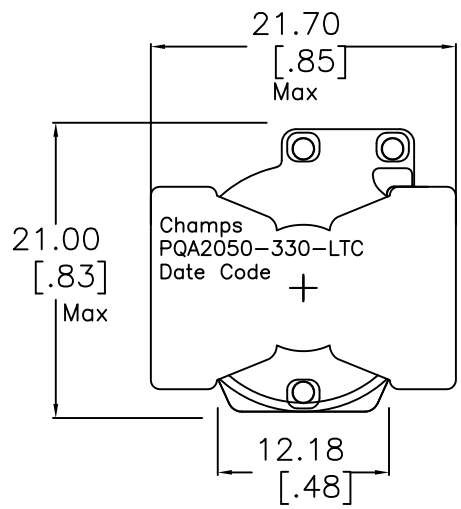
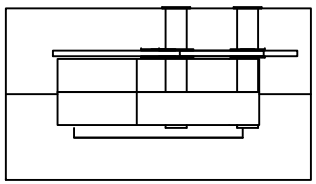


Pri & Secy Side Aux Only Supplied if called out in PN

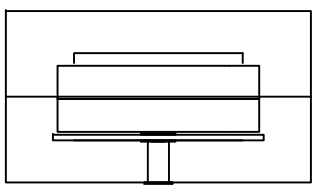
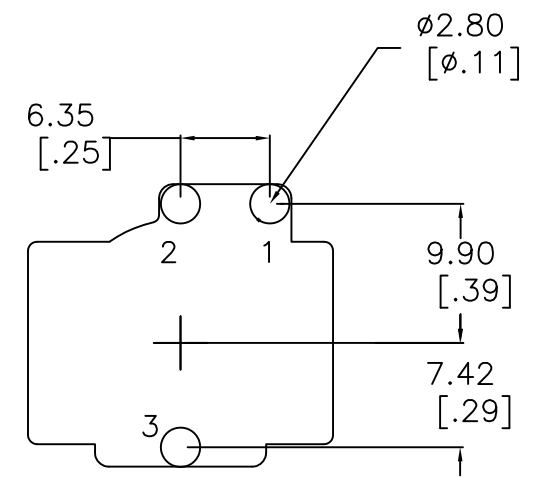
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							CHKD	PH			SIZE	SCALE 2:1	A
No.	DESCRIPTION	REVISIONS	DATE	APPR			APPR	DT					

1 2 3 4 5 6 7 8

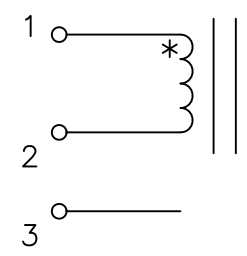
A
B
C
D
E
F



SUGGESTED
PAD
LAYOUT
Rounded
Pad



Schematic

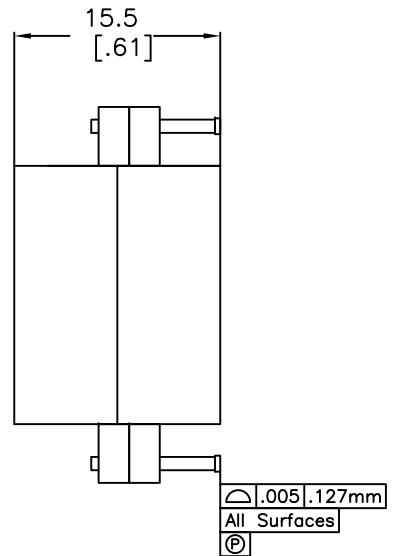
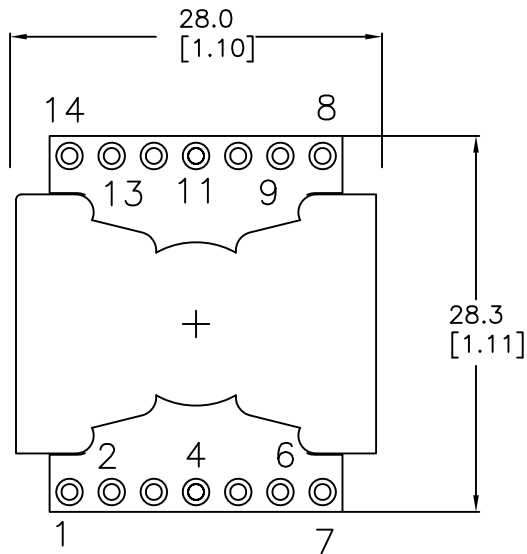


ELECTRICAL INFORMATION:

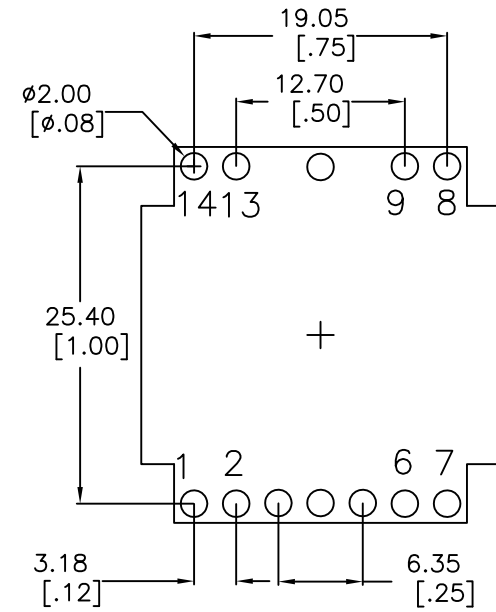
1. INDUCTANCE [1-2] = 330uH Nom, 305 Min. @100kHz/1V 2.15 Adc
2. DCR [1-2] = 93 mohms Nom, 105 Max
3. DIELECTRIC ISOLATION > 500 VDC [1-2] : CORE
4. SATURATION CURRENT @25C = 2.50 Adc | @85C = 2.25Adc
5. HEATING CURRENT FOR 40C RISE AT 25C AMBIENT = 4.75 Adc
6. Operating Ambient Temp. Range -40C to +100C
7. Non-Operating (Storage) Temp Range -55 to +130C
8. RoHS Level 6/6 Compliant

No.	DESCRIPTION	REVISIONS	DATE	APPR
CHAMPS TECHNOLOGIES				
DRAWN		SIGN	DATE	Champs No. PQA2050-330-LTC
CHKD		HE	8/20/08	Customer INDUCTOR ISSUE A REV 00
APPR			SIZE	SCALE 2:1

MECHANICAL DIMENSIONS [TOP VIEW]

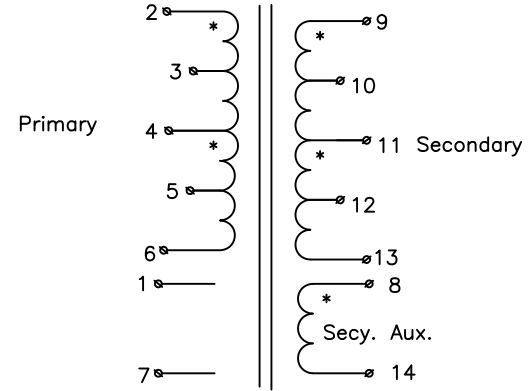


SUGGESTED PAD LAYOUT



8pls

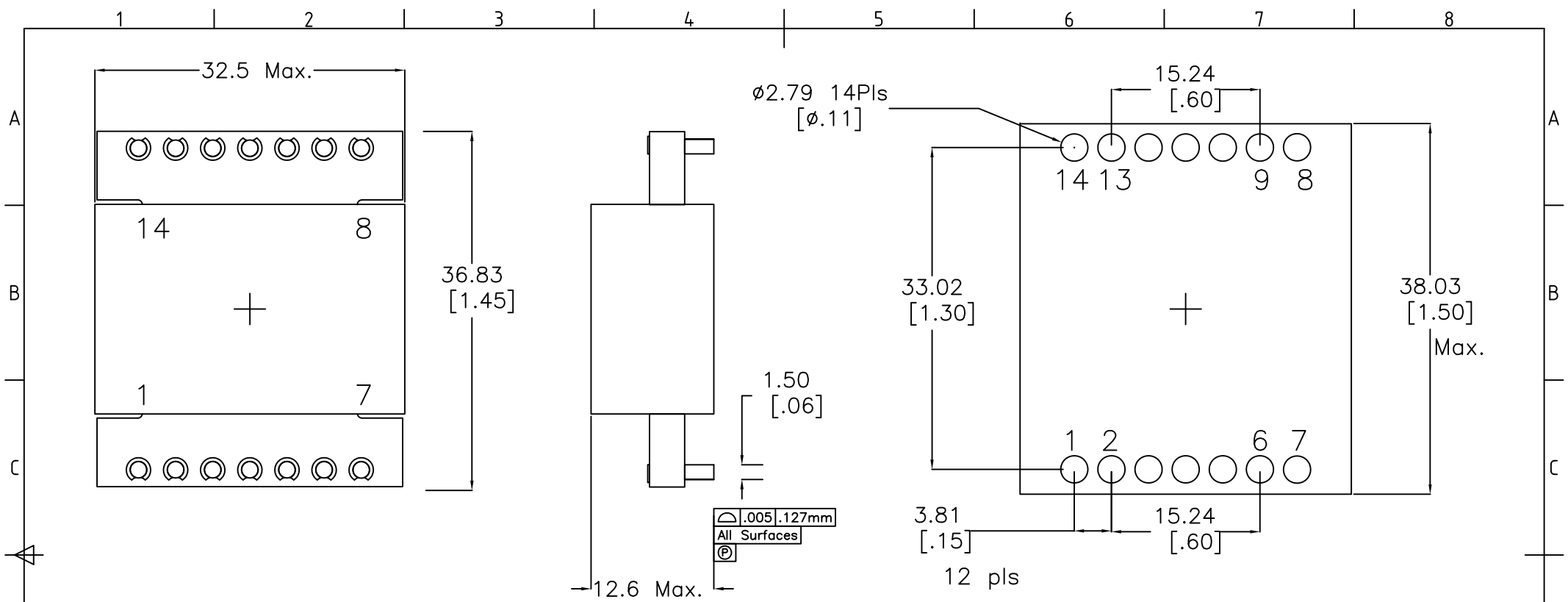
Schematic



Electrical Information:

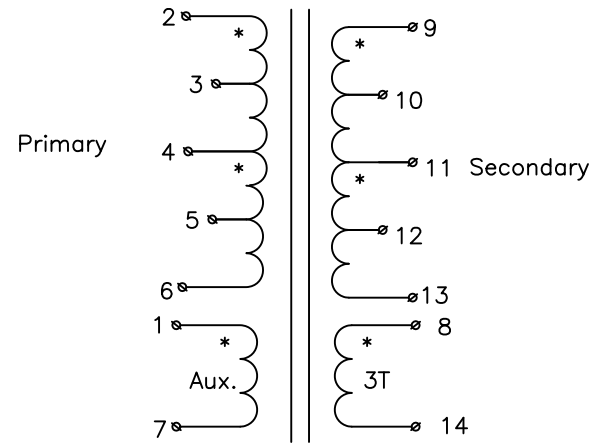
1. TURNS RATIO [2-6] : [9-13] 0.25 +/-2% || [8-14] : [9-13] = 0.188 +/-2%
2. INDUCTANCE [2-6] = 95 uH Nom @100kHz/1.0V
3. LEAKAGE INDUCTANCE [2-6] : SHORT 9-13 = 45 nH Nom @100kHz
4. DCR [2-6] = 4.6 mohms Nom, DCR [9-13] = 76 mohms Nom, DCR [8-14] = 300 mohm max
5. CAPACITANCE 2,6 to 9,13 = 130 pF Nom, 180 Max@100kHz
6. DIELECTRIC ISOLATION: [2,6]: [9,13][8,14], Core > 2250 Vdc
DIELECTRIC ISOLATION: [9,13][8,14] : CORE > 500 Vdc
7. RoHS Level 6/6 Compliant | Pins 96/4 Sn/Ag Plating
8. Operating Temp Range -55C to +130C [Inclusive of Application Temp Rise]
9. Storage Temp Range -55C to +130C [Materials rated to +170C]

No.		DESCRIPTION		REVISIONS	DATE	APPR
THIRD ANGLE PROJECTION						
CHAMPS TECHNOLOGIES						
TOLERANCES UNLESS OTHERWISE INDICATED		SIGN	DATE	Champs No. P26R6-0416-S03		
.XXX ± .25	.XX ± .51	DRAWN	JL	05.20.15	Customer	ISSUE A
.X ± 1.0	ANGLE ±	CHKD	PH		Part #:	REV 00
		APPR	DT		SIZE	SCALE 2:1



Electrical Information:

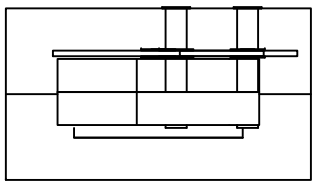
1. TURNS RATIO: [2-6] : [9-13] = 0.25 +/-2% || [8-14] : [9-13] = 0.187 +/-2%
2. INDUCTANCE [2-6] = 82 uH Nom, 64 Min @100kHz/1.0V
3. LEAKAGE INDUCTANCE [2-6] : SHORT 9-13 = 80nH Nom @100kHz
4. DCR [2-6] = 2.65 mohms Nom, DCR [9-13] = 37 mohms Nom, DCR [8-14] = 300 mohm max
5. CAPACITANCE 2,6 to 9,13 = 230 pF Nom @100kHz
6. DIELECTRIC ISOLATION: [2,6] : [9,13], [8,14] , Core > 2250 Vdc
DIELECTRIC ISOLATION: [9,13] : [8.14] : CORE > 500 Vdc
7. RoHS Level 6/6 Compliant | Pins 96/4 Sn/Ag Plating | REACH Compliant
- 8 Temp Rating: -55C to +130C [Inclusive of Temp Rise] || Materials to 180C



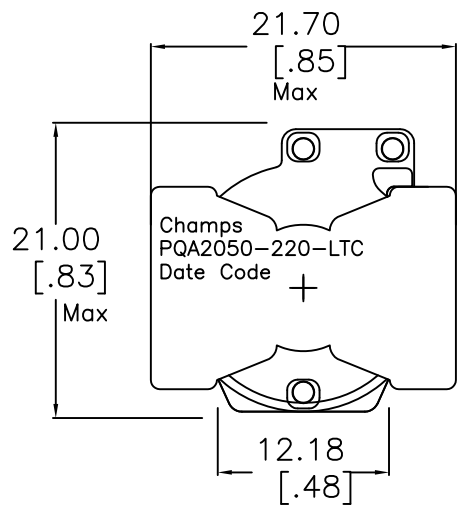
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							CHKD	PH			SIZE	SCALE 2:1	A
No.	DESCRIPTION	REVISIONS	DATE	APPR			APPR	DT					

1 2 3 4 5 6 7 8

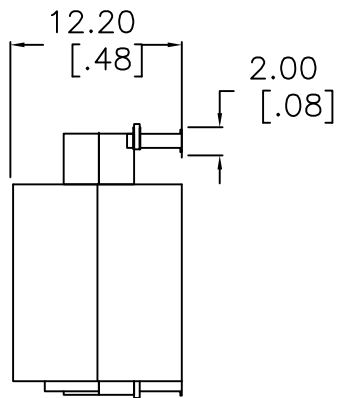
A



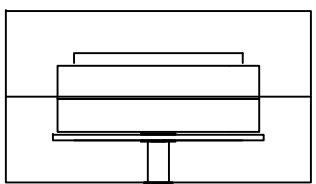
B



C

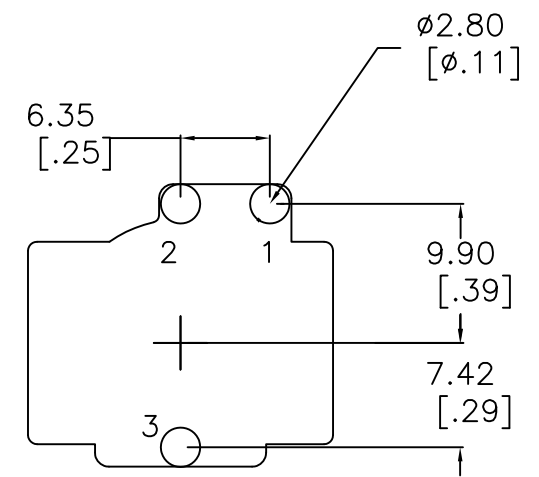


D



E

SUGGESTED
PAD
LAYOUT
Rounded
Pad

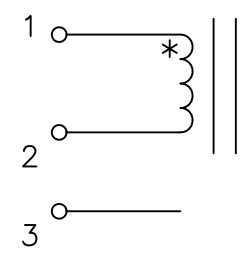


F

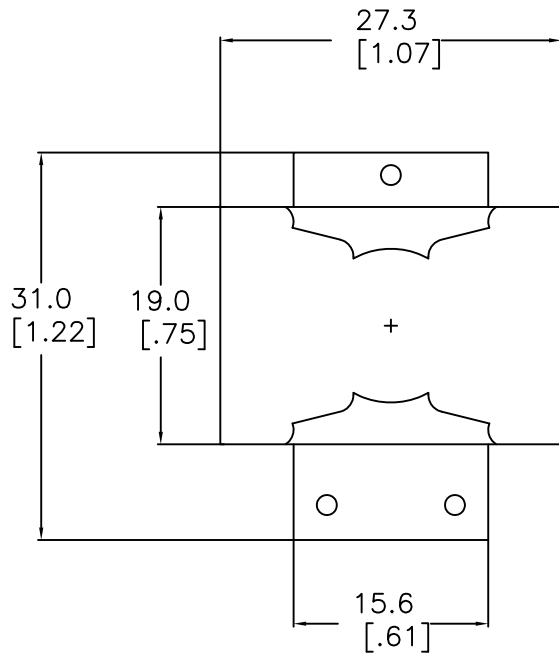
ELECTRICAL INFORMATION:

1. INDUCTANCE [1-2] = 220uH Nom, 198 Min. @100kHz/1V 2.5 Adc
2. DCR [1-2] = 54 mohms Nom, 62 Max
3. DIELECTRIC ISOLATION > 500 VDC [1-2] : CORE
4. SATURATION CURRENT @25C = 2.90 Adc | @100C = 2.65Adc
5. HEATING CURRENT FOR 40C RISE AT 25C AMBIENT = 5.75 Adc
6. Operating Ambient Temp. Range -40C to +85C
7. Non-Operating (Storage) Temp Range -55 to +130C
8. RoHS Level 6/6 Compliant

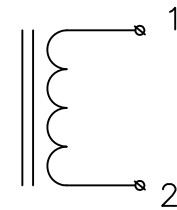
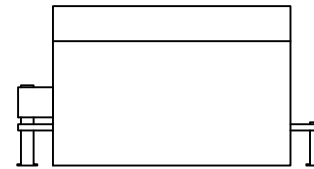
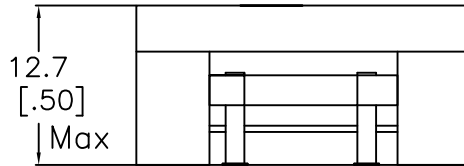
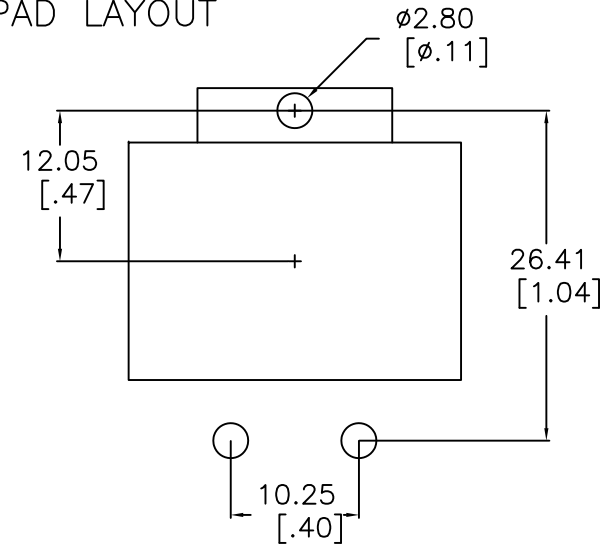
Schematic



No.		DESCRIPTION		REVISIONS	DATE	APPR
THIRD ANGLE PROJECTION						
CHAMPS TECHNOLOGIES						
TOLERANCES +/- 1.0 UNLESS OTHERWISE INDICATED		SIGN	DATE	Champs No. PQA2050-220-LTC		
DRAWN	HE	8/20/08	Customer		ISSUE	REV
CHKD			Part #: INDUCTOR		A	00
APPR			SIZE	SCALE 2:1		



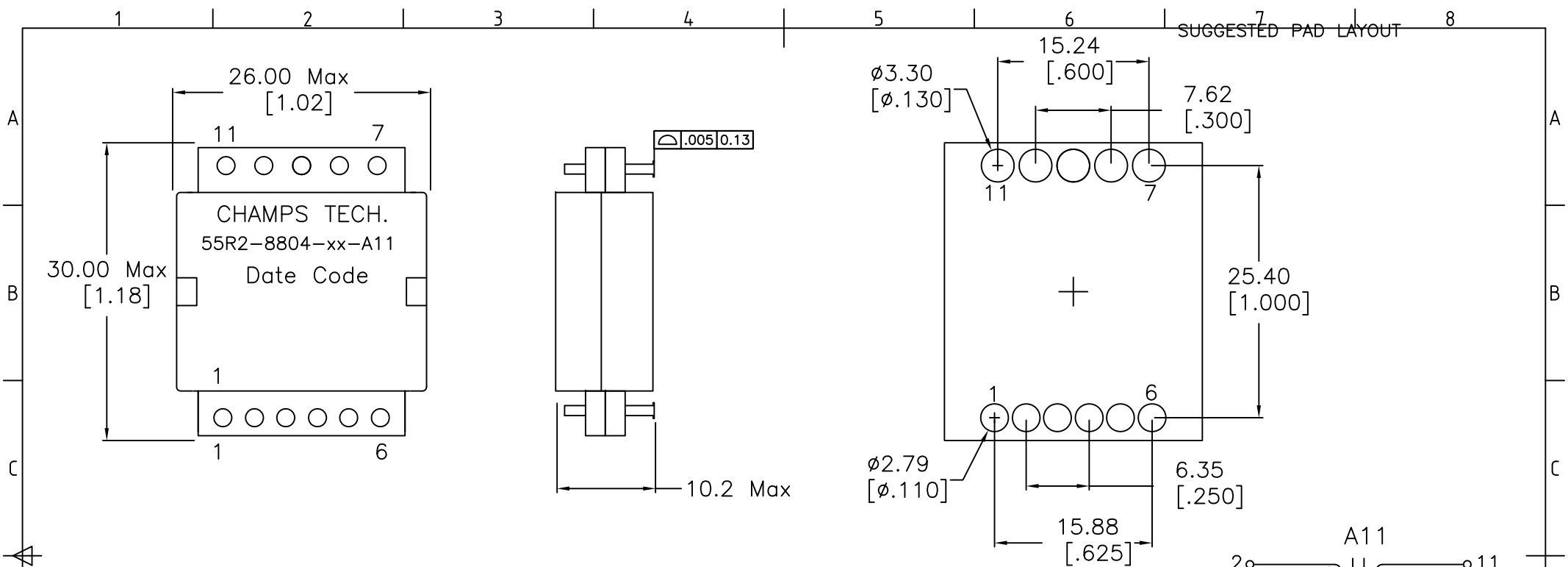
SUGGESTED PAD LAYOUT



NOTES:

1. INDUCTANCE [1-2] = 220 uH Nom, 198 Min. @100kHz 1.0V 5.0 Adc
2. INDUCTANCE [1-2] = 187 uH Min @100kHz 1.0V 6.0 Adc @25C
3. DCR [1-2] = 58 mohms Nom, 63 Max
4. DIELECTRIC ISOLATION > 500 VDC [1-2], : CORE
5. SATURATION CURRENT @25C = 6.0 Adc | @100C = 5.0 Adc
6. HEATING CURRENT FOR 45C RISE AT 25C AMBIENT = 6.8 Adc
7. Operating Ambient Temperature: -55C to +130C [Inclusive of Temps Rise]
8. RoHS Level 6/6 Compliance || 96/4 Sn/Ag Pin Composition

No.	DESCRIPTION	REVISIONS	DATE	APPR
THIRD ANGLE PROJECTION				
CHAMPS TECHNOLOGIES				
DRAWN		SIGN	DATE	Champs No. PQI26-220-LTC
CHKD				Customer ISSUE
APPR			08.17.15	Part #: A REV 00
TOLERANCES +/- 1.0 UNLESS OTHERWISE INDICATED			SIZE	SCALE 2:1

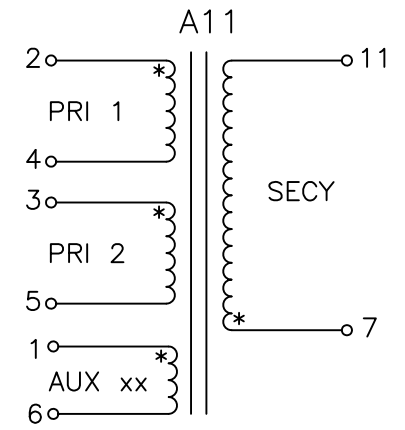


ELECTRICAL INFORMATION:

1. TURNS RATIO [7-11] : [2-4] = [3-5] = 0.50 +/-2%
2. DCR [2-4]= [3-5]=28.0 mohm Nom., [7-11]= 3.5 mohm Nom, [1-6]= 300 mohm Max.
3. Inductance [2-5] = 1130uH Nom, 850 uH Min @10KHz, 0.1 VRMS @ 25C
4. Leakage Inductance [2-5]= Short [7-11] = 450nH Max @100 KHz
5. Dielectric Strength 6 Sec Min: [1,2,5,6] to [7,11] 1500 VDC | [2,5] to [1,6] 500 VDC
[1,2,5,6] to CORE 1500 VDC, [7,11] to CORE 500 VDC
6. Weight 18 grams Max | RoHS Compliant | Pin Composition Tin/Silver Plating

ORDERING INFORMATION:

1. Order Per Part # 55R2-8804-xx-A11. Parts ship in trays unless otherwise specified.
2. 25 parts per tray.

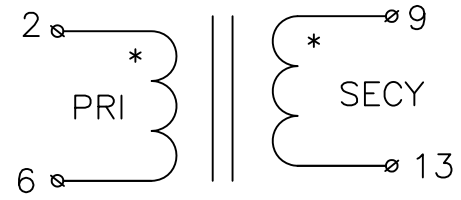
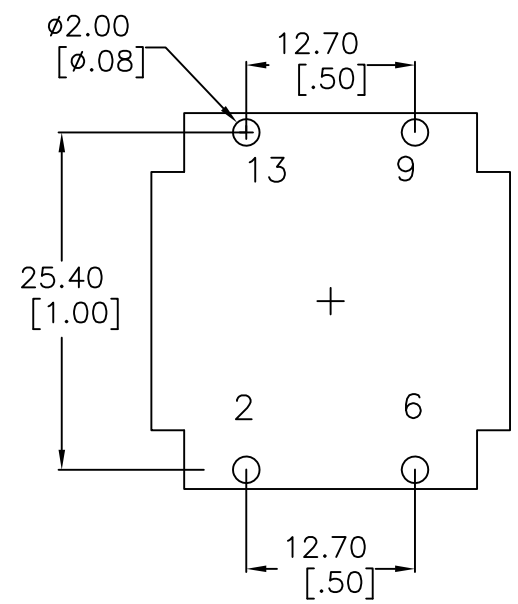
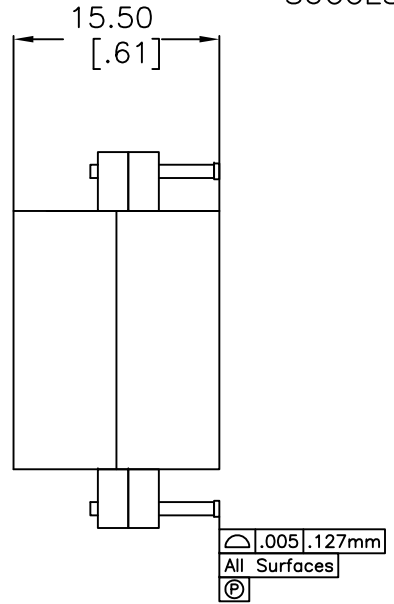
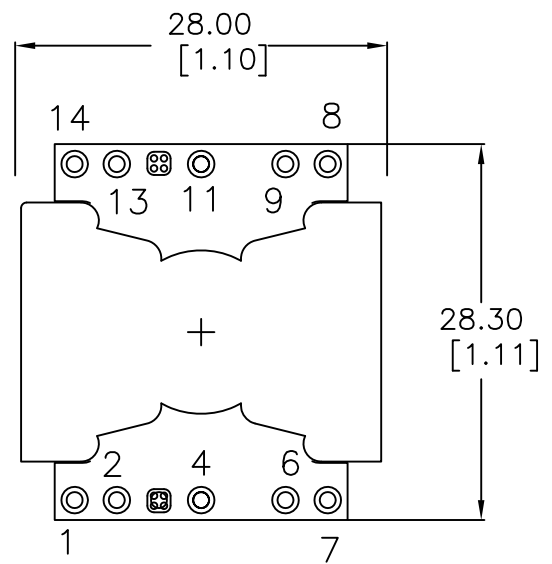


No.	DESCRIPTION	REVISIONS	DATE	APPR
THIRD ANGLE PROJECTION				
CHAMPS TECHNOLOGIES				
TOLERANCES +/- 1.0 UNLESS OTHERWISE INDICATED		SIGN	DATE	Champs No. 55R2-8804-xx-A11
DRAWN	HE	7/22/08	Customer	ISSUE A
CHKD			Part #:	REV 00
APPR			SIZE	SCALE 2:1

1 2 3 4 5 6 7 8

A
B
C
D
E
F

SUGGESTED PAD LAYOUT

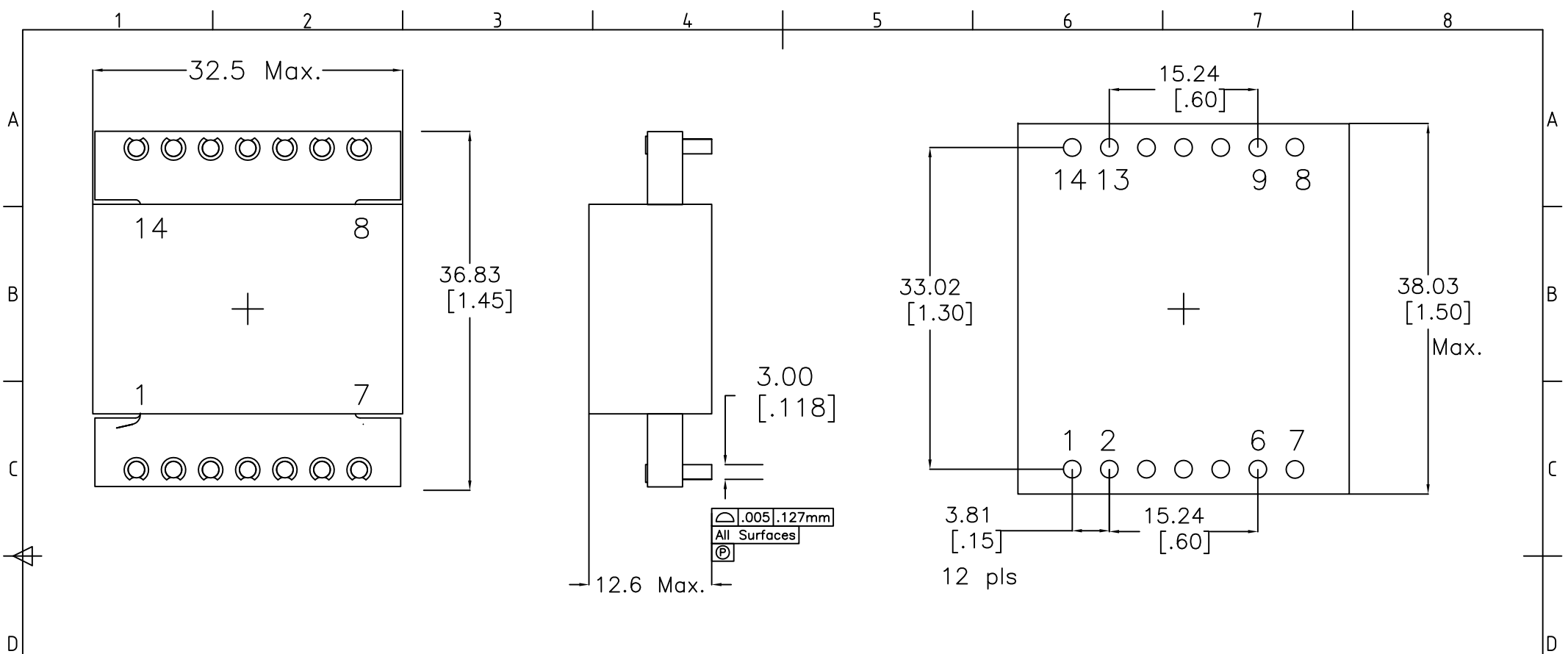


Electrical Information:

1. TURNS RATIO [2-6] : [9-13] = 0.428 +/-2%
2. INDUCTANCE [2-6] = 230 uH Nom @100kHz/1.0V
3. LEAKAGE INDUCTANCE [2-6] : SHORT 9-13 = 80 nH Nom @100kHz
4. DCR [2-6] = 11.6 mohms Nom, DCR [9-13] = 65 mohms Nom,
5. CAPACITANCE 2,6 to 9,13 = 270 pF Max @100kHz
6. DIELECTRIC ISOLATION :1500 VDC [1,2] : [9-13] || 1500 VDC CORE :[1,2]
DIELECTRIC ISOLATION 500 VDC CORE :[9-13]
7. RoHS Level 6/6 Compliant | Pins 96/4 Sn/Ag Plating

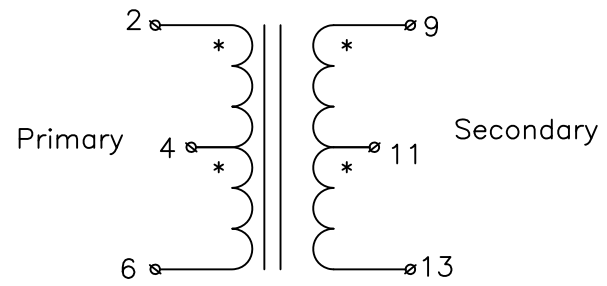
APPLICATION: 36-72Vin to 54Vout@3A FORWARD

No.		DESCRIPTION		REVISIONS	DATE	APPR
THIRD ANGLE PROJECTION						
CHAMPS TECHNOLOGIES						
TOLERANCES +/- 1.0 UNLESS OTHERWISE INDICATED		SIGN	DATE	Champs No. P26R2-0614		
.XXX ±	DRAWN	JL	11.26.14	Customer	ISSUE	REV
.XX ±	CHKD	PH		Part #:	A	00
.X ANGLE ±	APPR	DT		SIZE	SCALE 2:1	



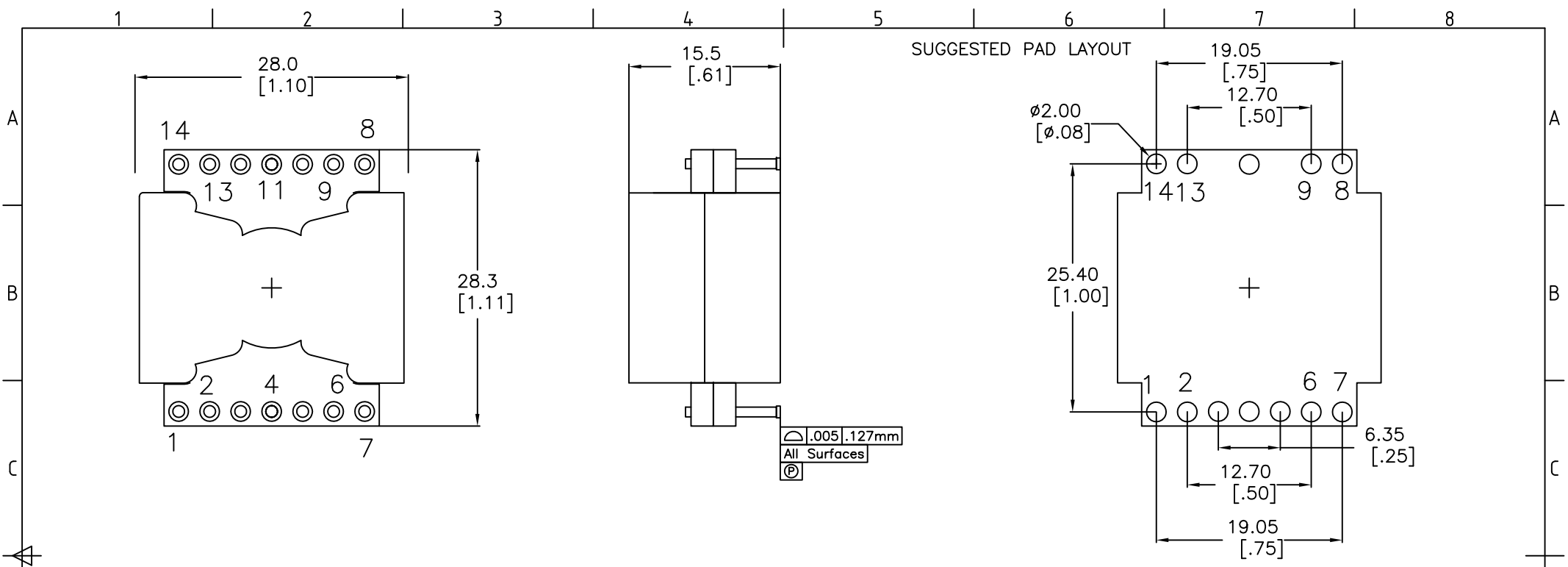
Electrical Information:

1. TURNS RATIO [2-6] : [9-13] = 0.428 +/-2%
2. INDUCTANCE [2-6] = 190 uH Nom, 145 Min @100kHz/1.0V
3. LEAKAGE INDUCTANCE [2-6] : SHORT 9-13 = 60nH Max @100kHz
4. DCR [2-6] = 7.0 mohms Nom, DCR [9-13] = 41.5 mohms Nom,
5. CAPACITANCE 2,6 to 9,13 = 120 pF Max @100kHz
6. DIELECTRIC ISOLATION: [2,6] : [9,13] > 2500 Vrms
DIELECTRIC ISOLATION: [9,13] : CORE > 500 Vdc
7. RoHS Level 6/6 Compliant | Pins 96/4 Sn/Ag Plating



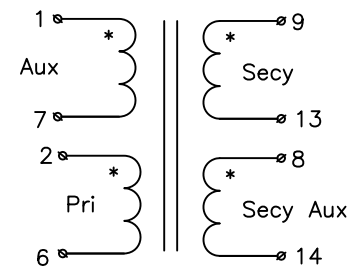
APPLICATION 36-72VIN TO 54VOUT @3A FORWARD

					CHAMPS TECHNOLOGIES	TOLERANCES UNLESS OTHERWISE INDICATED .XXX ± .XX ± .X ±	DRAWN	JL	11/18/14	TITLE: 80R2-0614		ISSUE	REV
							CHKD	PH			SIZE	SCALE 2:1	A
No.	DESCRIPTION	REVISIONS	DATE	APPR			APPR	DT					



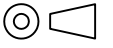
Δ .005 | .127mm
 All Surfaces
 $\text{\textcircled{P}}$

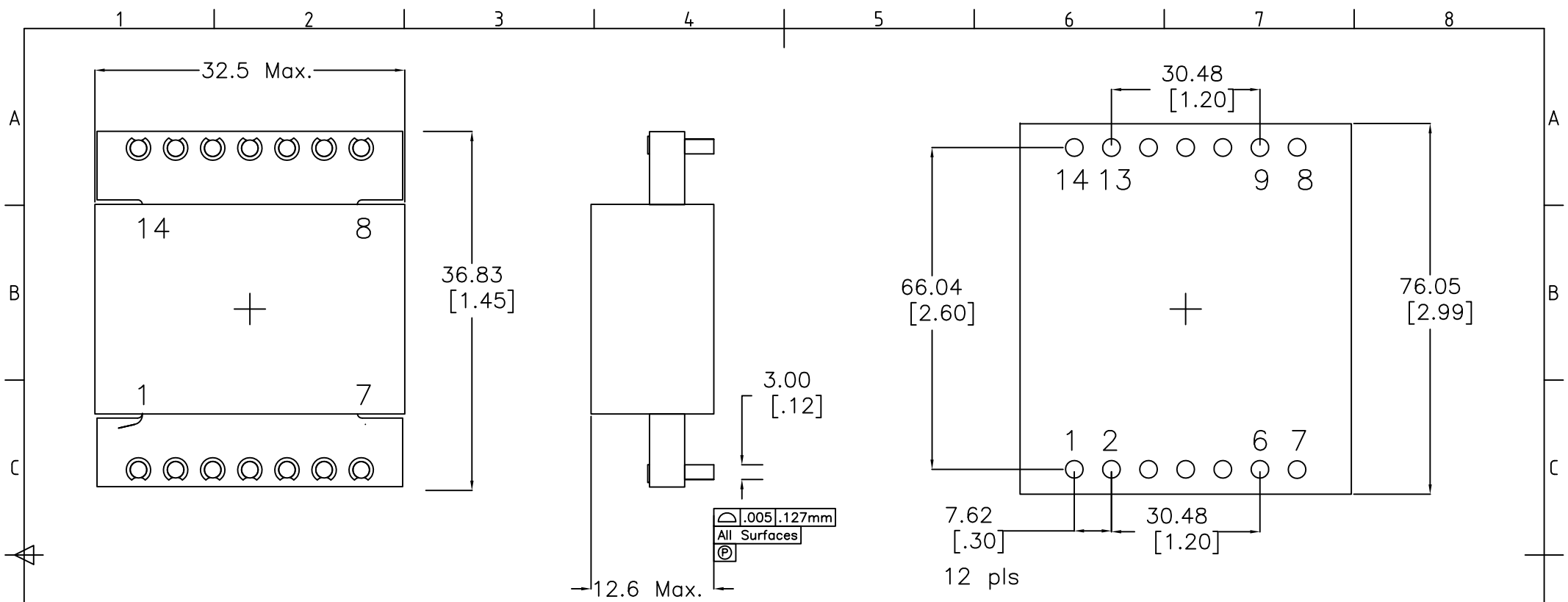
Schematic



Electrical Information:

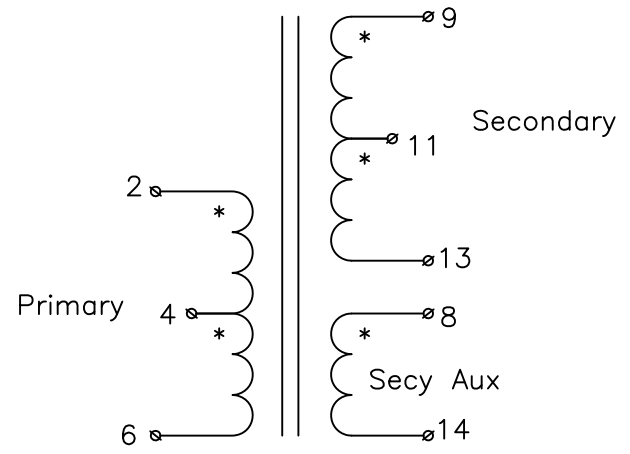
1. TURNS RATIO [2-6] : [9-13] 0.571 +/--2%
 TURNS RATIO [1-7] : [9-13] 0.143 +/--2% || [8-14] : [9-13] = 0.072
2. INDUCTANCE [2-6] = 384 uH Nom @100kHz/1.0V
3. LEAKAGE INDUCTANCE [2-6] : SHORT 9-13 = 200 nH Nom @100kHz
4. DCR [2-6] = 17.7 mohms Nom, DCR [9-13] = 61 mohms Nom,
 DCR [2-6] & [8-14] = 300 mohm max
5. CAPACITANCE 2,6 to 9,13 = 130 pF Nom, 180 Max@100kHz
6. DIELECTRIC ISOLATION: [2,6]: [9,13][8,14] > 1500 VDC
 DIELECTRIC ISOLATION: [9,13][8,14] : CORE > 500 Vdc
7. RoHS Level 6/6 Compliant | Pins 96/4 Sn/Ag Plating
8. Operating Temp Range -55C to +130C [Inclusive of Application Temp Rise]
9. Storage Temp Range -55C to +130C [Materials rated to +170C]

No.		DESCRIPTION		REVISIONS	DATE	APPR
THIRD ANGLE PROJECTION						
						
CHAMPS TECHNOLOGIES						
TOLERANCES UNLESS OTHERWISE INDICATED		SIGN	DATE	Champs No. P26R6-0814-02-S01		
.XXX ± .25	.XX ± .51	JL	05.20.15	Customer		ISSUE A
.X ± 1.0	ANGLE ±	PH		Part #:		REV 00
		APPR	DT	SIZE	SCALE 2:1	



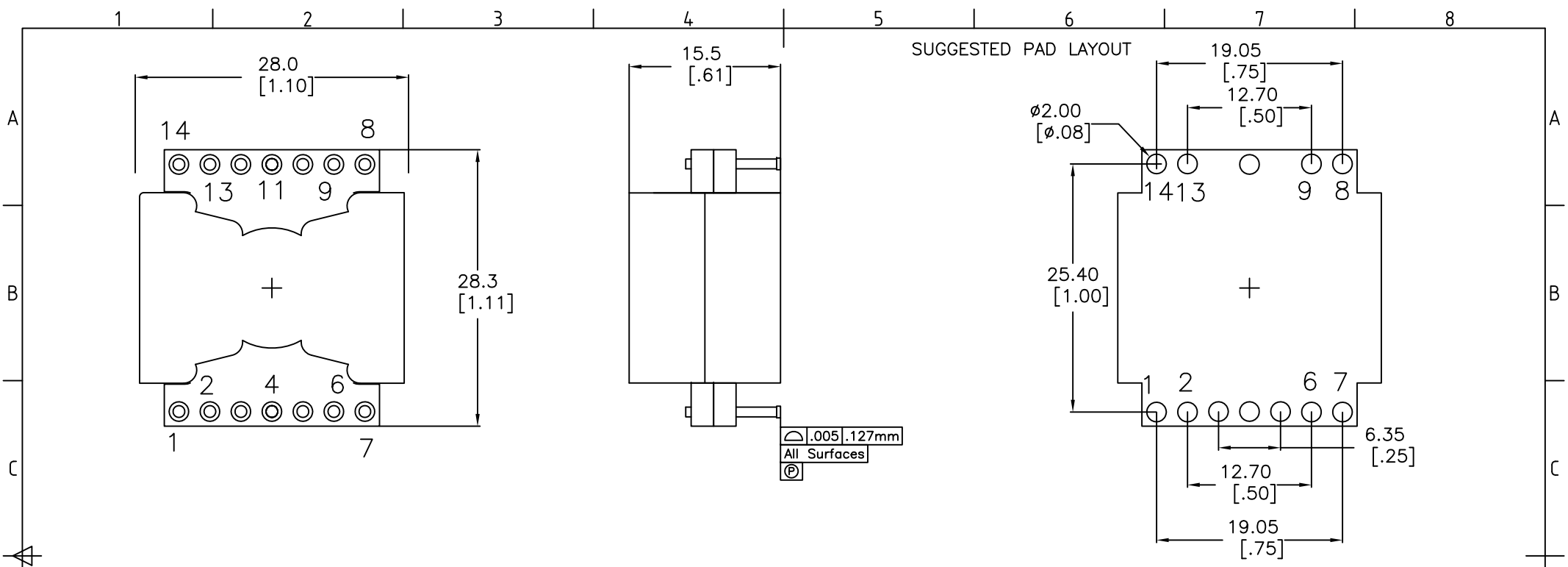
Electrical Information:

1. TURNS RATIO [2-6] : [9-13] = 0.571 +/-2% || [8-14] : [9-13] = 0.143
2. INDUCTANCE [2-6] = 342 uH Nom, 258 Min @100kHz/1.0V
3. LEAKAGE INDUCTANCE [2-6] : SHORT 9-13 = 250nH Nom @100kHz
4. DCR [2-6] = 11 mohms Nom, DCR [9-13] = 37 mohms Nom, DCR [8-14] = 300 mohm max
5. CAPACITANCE 2,6 to 9,13 = 120 pF Nom @100kHz
6. DIELECTRIC ISOLATION: [2,6] : [9,13], [[8,14] > 2500 Vrms
DIELECTRIC ISOLATION: [9,13} : [8.14] : CORE > 500 Vdc
7. RoHS Level 6/6 Compliant | Pins 96/4 Sn/Ag Plating



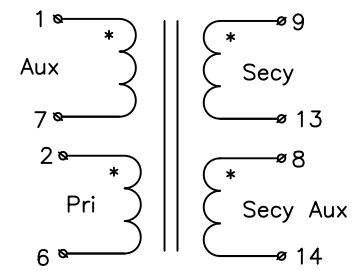
APPLICATION 50-150VIN TO 54VOUT @3.0A FORWARD

				CHAMPS TECHNOLOGIES	TOLERANCES UNLESS OTHERWISE INDICATED .XXX ± .XX ± ANGLE ±	DRAWN	JL	1/27/15	TITLE: 80R6-0814-S02		ISSUE	REV
						CHKD	PH			SIZE	SCALE 2:1	A
No.	DESCRIPTION	REVISIONS	DATE	APPR		APPR	DT					



Δ .005|.127mm
 All Surfaces
 $\text{\textcircled{P}}$

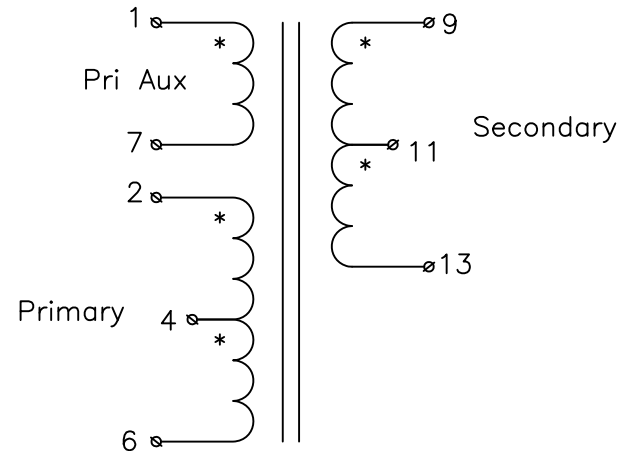
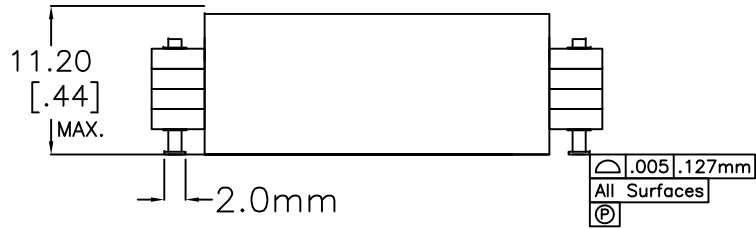
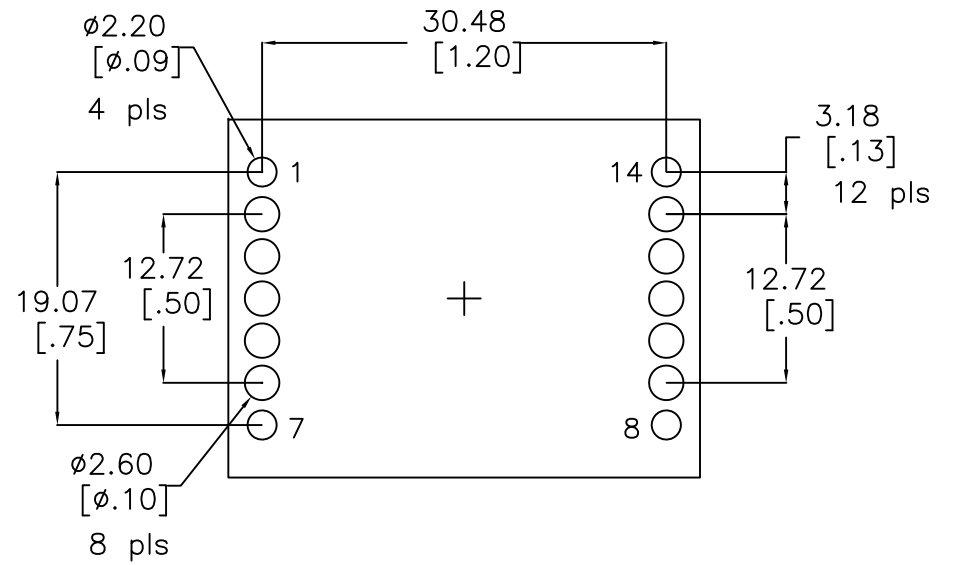
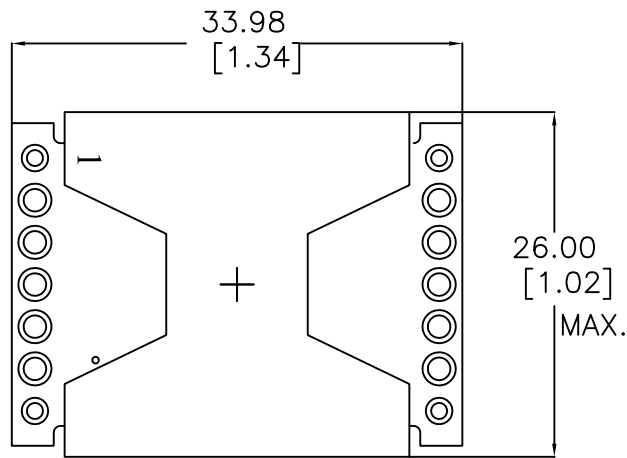
Schematic



Electrical Information:

1. TURNS RATIO [2-6] : [9-13] 0.625 +/--2%
 TURNS RATIO [1-7] : [9-13] 0.125 +/--2% || [8-14] : [9-13] = 0.062
2. INDUCTANCE [2-6] = 585 uH Nom @100kHz/1.0V
3. LEAKAGE INDUCTANCE [2-6] : SHORT 9-13 = 500 nH Nom @100kHz
4. DCR [2-6] = 32.4 mohms Nom, DCR [9-13] = 77 mohms Nom,
 DCR [2-6] & [8-14] = 300 mohm max
5. CAPACITANCE 2,6 to 9,13 = 150 pF Nom, 190 Max@100kHz
6. DIELECTRIC ISOLATION: [2,6]: [9,13][8,14] > 1500 VDC
 DIELECTRIC ISOLATION: [9,13][8,14] : CORE > 500 Vdc
7. RoHS Level 6/6 Compliant | Pins 96/4 Sn/Ag Plating
8. Operating Temp Range -55C to +130C [Inclusive of Application Temp Rise]
9. Storage Temp Range -55C to +130C [Materials rated to +170C]

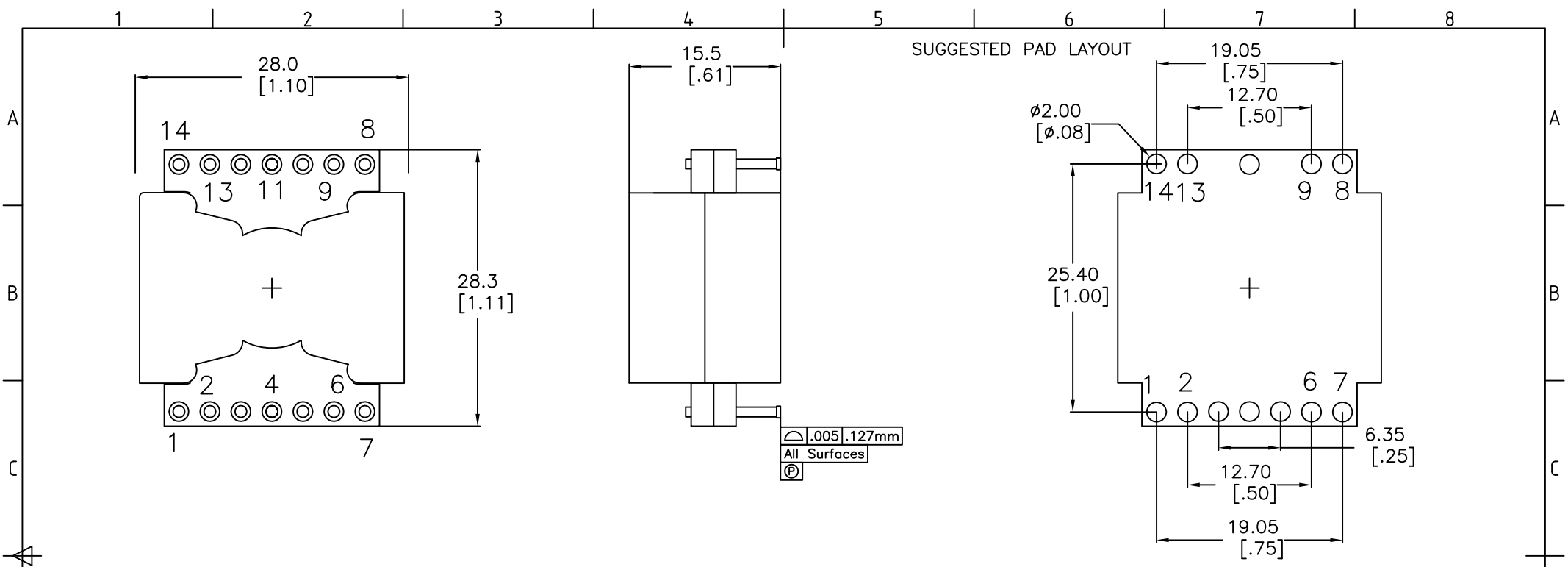
No.		DESCRIPTION		REVISIONS	DATE	APPR
CHAMPS TECHNOLOGIES						
THIRD ANGLE PROJECTION						
TOLERANCES UNLESS OTHERWISE INDICATED		SIGN	DATE	Champs No. P26R6-1016-02-S01		
.XXX ± .25	DRAWN	JL	05.20.15	Customer		ISSUE
.XX ± .51	CHKD	PH		Part #:		A
.X ± 1.0	APPR	DT		SIZE	SCALE 2:1	REV 00
ANGLE ±						



Electrical Information:

1. TURNS RATIO [2-6] : [9-13] = 0.462 +/-2% || [1-7] : [2-6] = 0.25 +/-2%
2. INDUCTANCE [2-6] = 480 uH Nom @100kHz/1.0V
3. LEAKAGE INDUCTANCE [2-6] : SHORT 9-13 = 400 nH Max @100kHz
4. DCR [2-6] = 28 mohms Nom, DCR [9-13] = 140 mohms Nom, DCR [1-7] = 300 mohm max
5. CAPACITANCE 2,6 to 9,13 = 90 pF Max @100kHz
6. DIELECTRIC ISOLATION: [2,6],[1,7] : [9,13] > 2250 Vdc
DIELECTRIC ISOLATION: [9,13] : CORE > 500 Vdc
7. RoHS Level 6/6 Compliant | Pins 96/4 Sn/Ag Plating

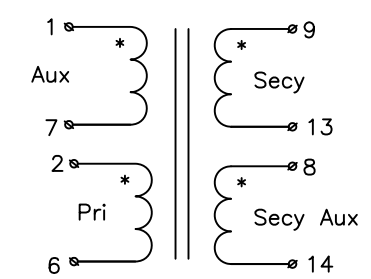
					CHAMPS TECHNOLOGIES	TOLERANCES UNLESS OTHERWISE INDICATED .xxx ± .xx ± .x ±	DRAWN	JL	11/18/14	TITLE: D26R6-1226-03		ISSUE	REV
							CHKD	PH			SIZE	SCALE 2:1	A
No.	DESCRIPTION	REVISIONS	DATE	APPR			APPR	DT					



Electrical Information:

1. TURNS RATIO [9-13] : [2-6] = 0.778 +/-2%
2. INDUCTANCE [2-6] = 2050 uH Nom @100kHz/1.0V
3. LEAKAGE INDUCTANCE [2-6] : SHORT 9-13 = 1.5 uH Nom @100kHz
4. DCR [2-6] = 105 mohms Nom, DCR [9-13] = 61 mohms Nom, DCR [1-7] = [8-14] = 300 mohm max
5. CAPACITANCE 2,6 to 9,13 = 90 pF Nom @100kHz
6. DIELECTRIC ISOLATION: [2,6]: [9,13][8,14] > 1500 VDC
DIELECTRIC ISOLATION: [9,13][8,14] : CORE > 500 Vdc
7. RoHS Level 6/6 Compliant | Pins 96/4 Sn/Ag Plating
8. Operating Temp Range -55C to +130C [Inclusive of Application Temp Rise]
9. Storage Temp Range -55C to +130C [Materials rated to +170C]

Schematic



No.		DESCRIPTION		REVISIONS	DATE	APPR
CHAMPS TECHNOLOGIES						
THIRD ANGLE PROJECTION		CHAMPS No. P26R6-1814				
TOLERANCES UNLESS OTHERWISE INDICATED		DRAWN	SIGN	DATE	Customer	
.XXX ± .25	.XX ± .51	CHKD	PH	05.20.15	Part #:	ISSUE A
.X ±	ANGLE ±	APPR	DT	SCALE 2:1	REV 00	