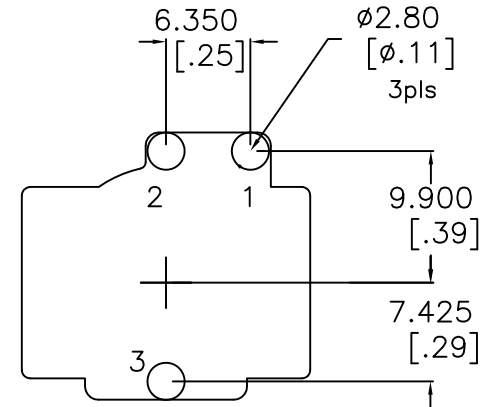
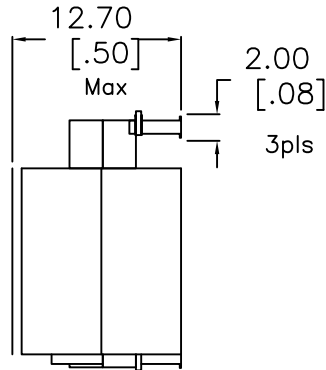
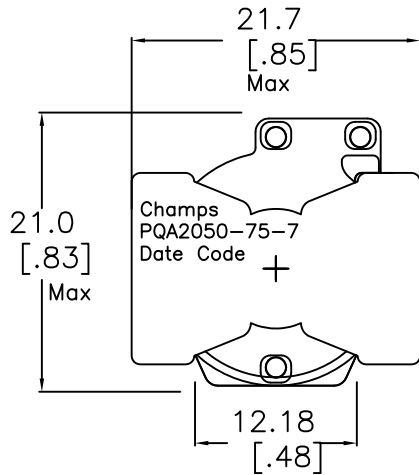
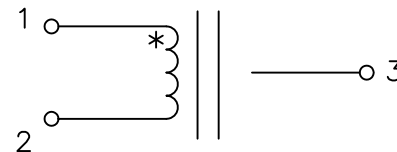


SUGGESTED PAD LAYOUT [TOP PCB VIEW]



Schematic



ELECTRICAL INFORMATION:

1. INDUCTANCE [1-2] = 75uH Nom, 67 Min. @100kHz/1V 6.5 Adc
2. DCR [1-2] = 53 mohms Nom, 58 Max
3. DIELECTRIC ISOLATION > 500 VDC [1-2] : CORE
4. SATURATION CURRENT @25C = 7.5 Adc | @85C = 7.0 Adc
5. HEATING CURRENT FOR 40C RISE AT 25C AMBIENT = 7.0 Adc
6. Operating Ambient Temp. Range -55C to +130C [Inclusive of Temp Rise]
7. RoHS Level 6/6 Compliant || REACH Compliant

No.	DESCRIPTION	REVISIONS	DATE	APPR
THIRD ANGLE PROJECTION				
CHAMPS TECHNOLOGIES				
TOLERANCES/UNITS in MM UNLESS OTHERWISE INDICATED		SIGN	DATE	Champs-Tech PN PQA2050-75-7-LTC
.XXX ± 0.254	DRAWN	HE	8/20/08	Customer
.XX ± 0.51	CHKD			Part #:
.X ± 0.78	APPR			INDUCTOR
ANGLE ±				ISSUE A
				REV 00
				SCALE 2:1